

Fig. 1

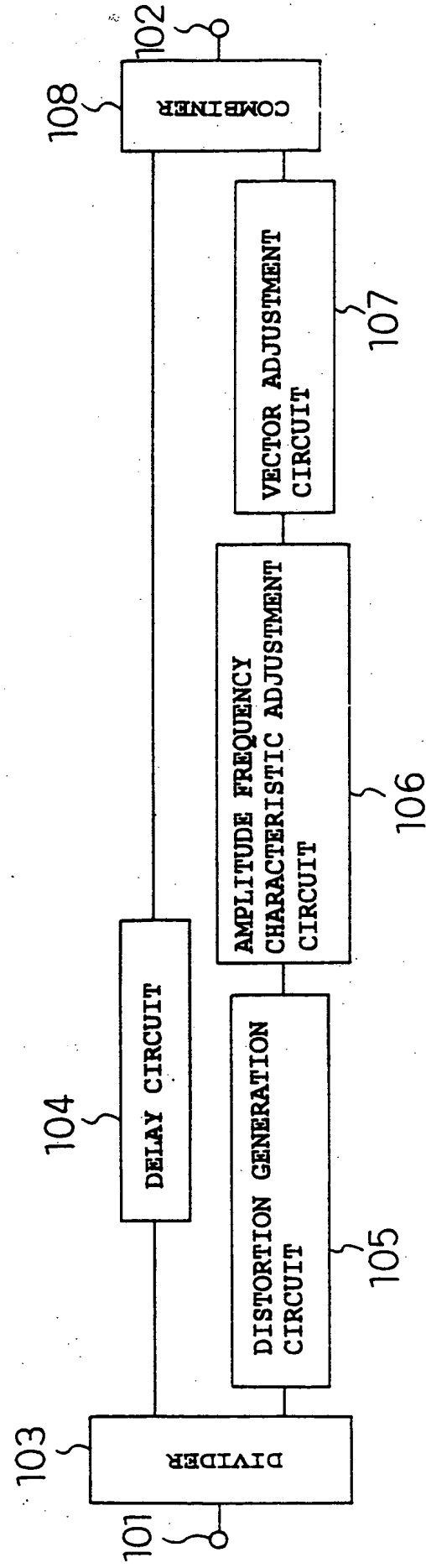


Fig. 2 A

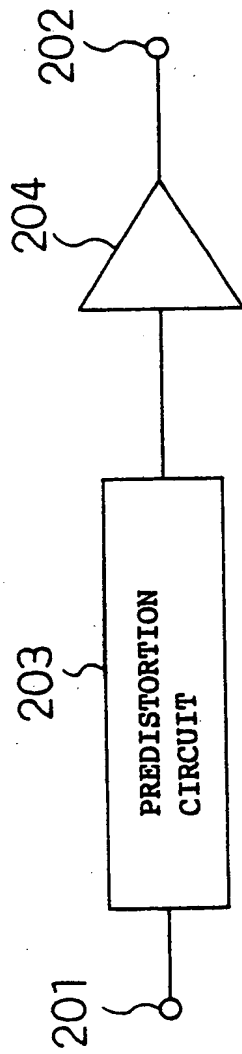
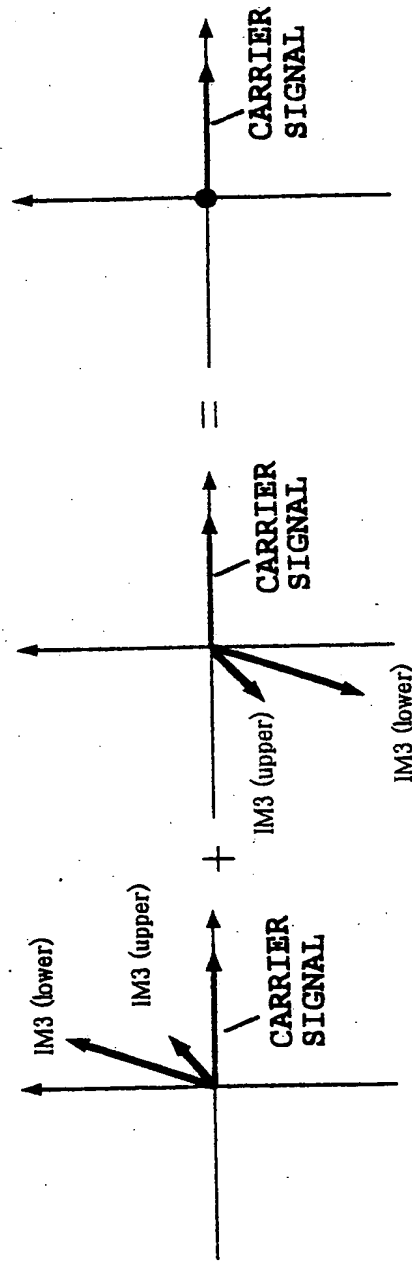


Fig. 2 B



<p>THIRD-ORDER INTERMODULATION DISTORTION GENERATED BY PREDISTORTION CIRCUIT 203</p>	<p>THIRD-ORDER INTERMODULATION DISTORTION GENERATED BY POWER AMPLIFIER 204</p>	<p>TOTAL DISTORTION CHARACTERISTIC</p>
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Fig. 3A

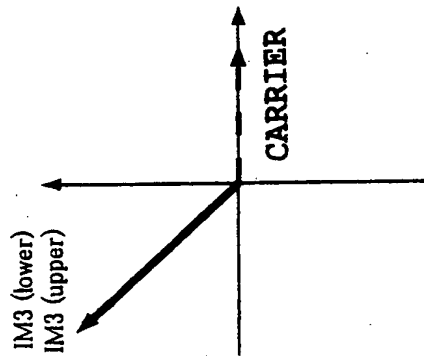


Fig. 3B

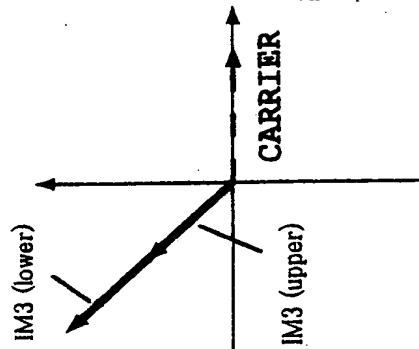
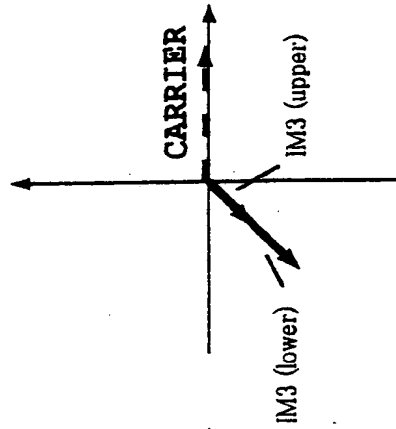


Fig. 3C

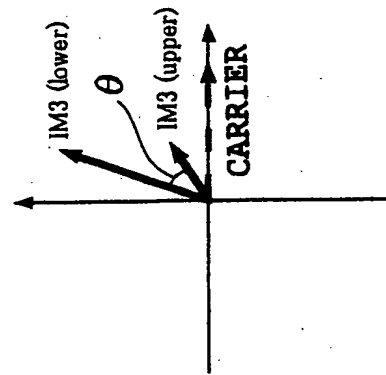


ONLY DISTORTION
GENERATION CIRCUIT

(DISTORTION GENERATION
CIRCUIT) + (AMPLITUDE
ADJUSTMENT CIRCUIT)

(DISTORTION GENERATION CIRCUIT) +
(AMPLITUDE ADJUSTMENT CIRCUIT) +
(VECTOR ADJUSTMENT CIRCUIT)

Fig. 3D



(DISTORTION GENERATION CIRCUIT) + (AMPLITUDE ADJUSTMENT
CIRCUIT) + (VECTOR ADJUSTMENT CIRCUIT) + (DELAY DIFFERENCE)

Fig. 4A

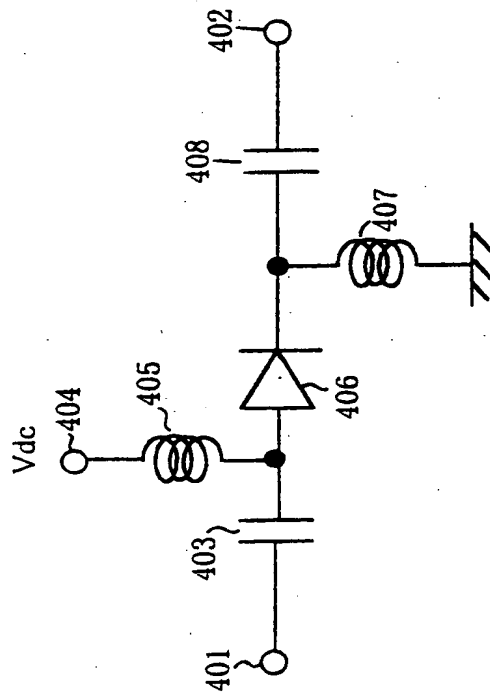


Fig. 4B

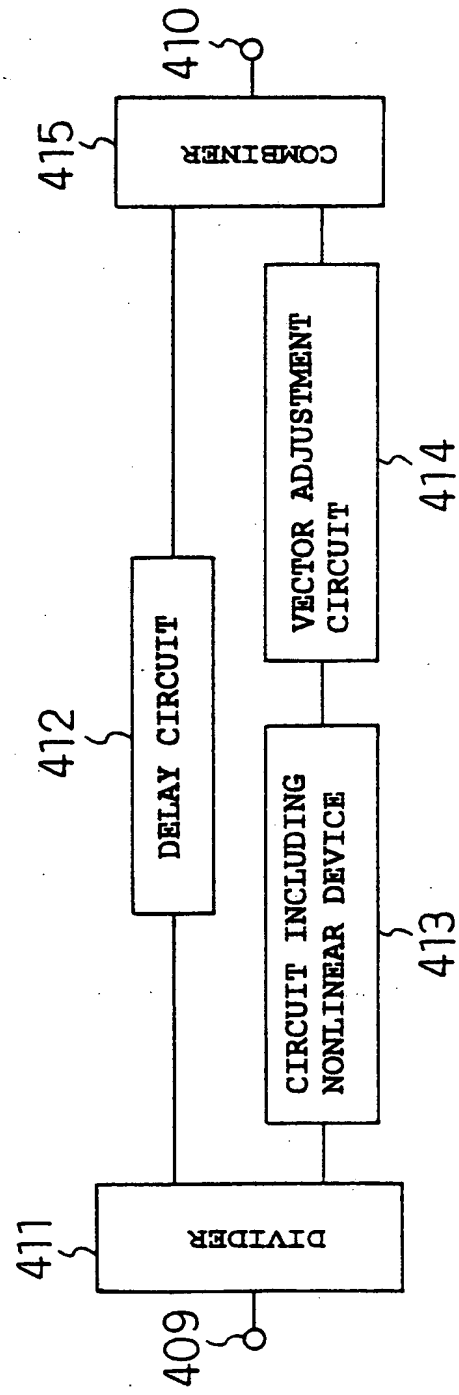


Fig. 5

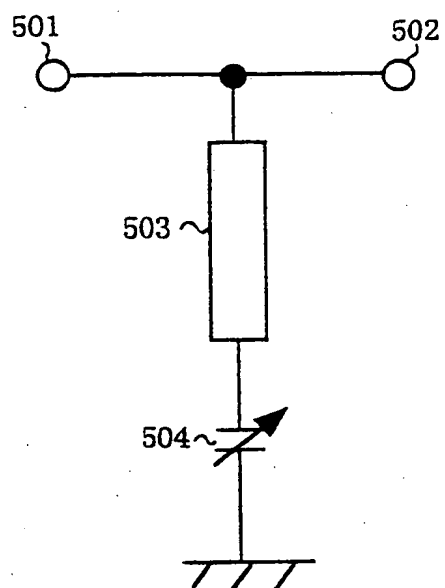


Fig. 6

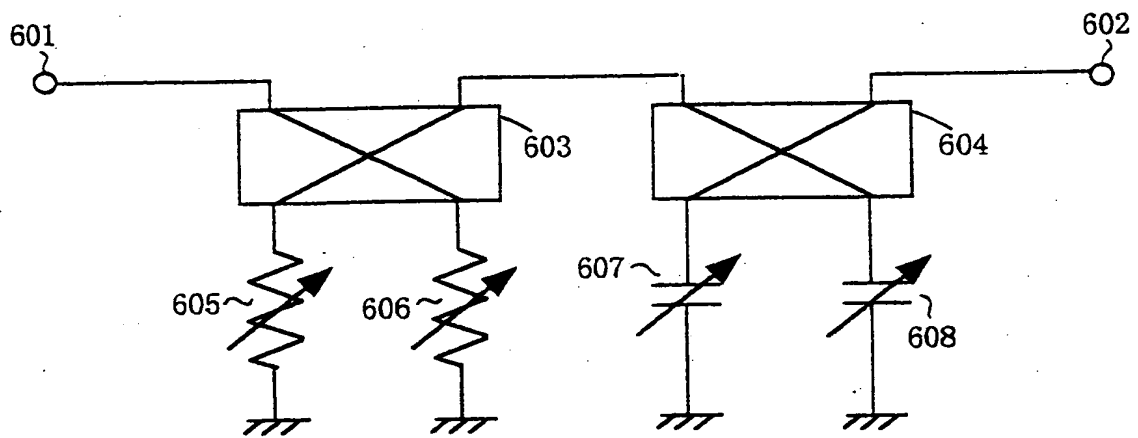


Fig. 7

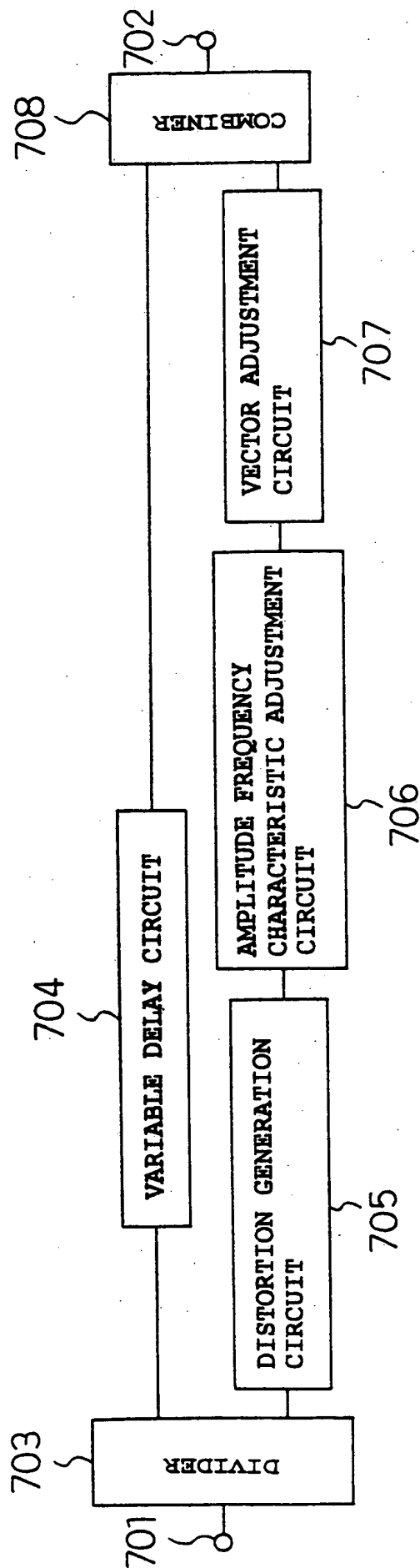


Fig. 8

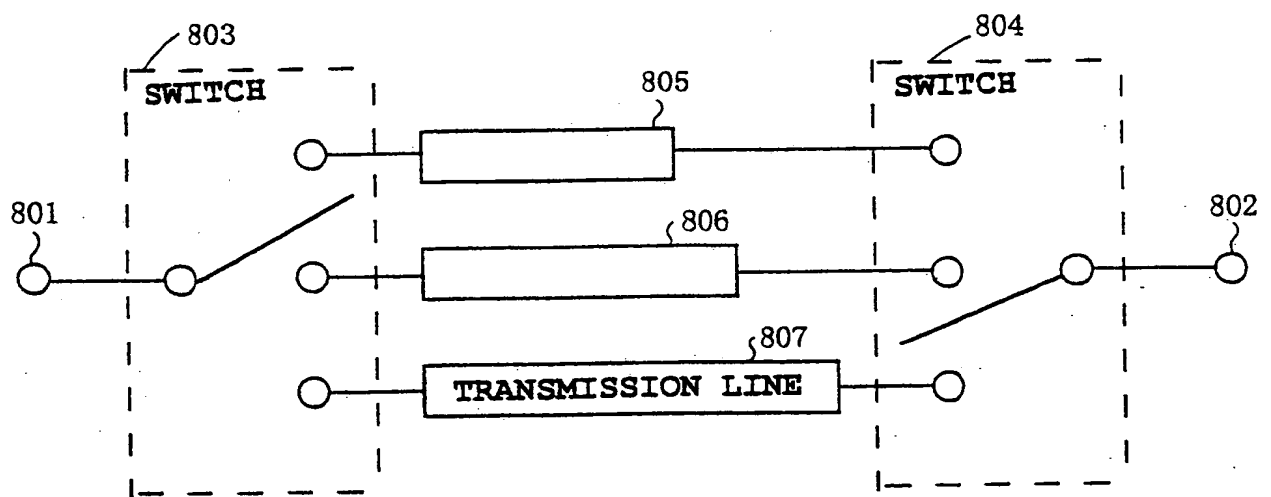


Fig. 9

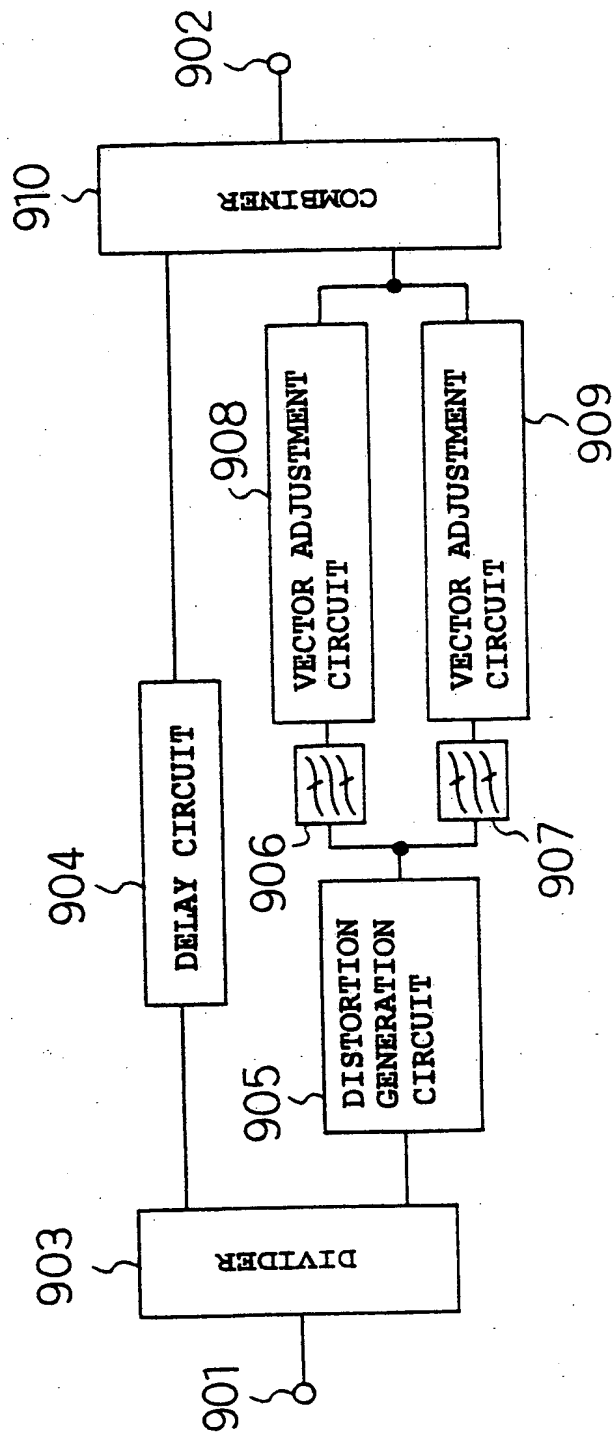


Fig. 10

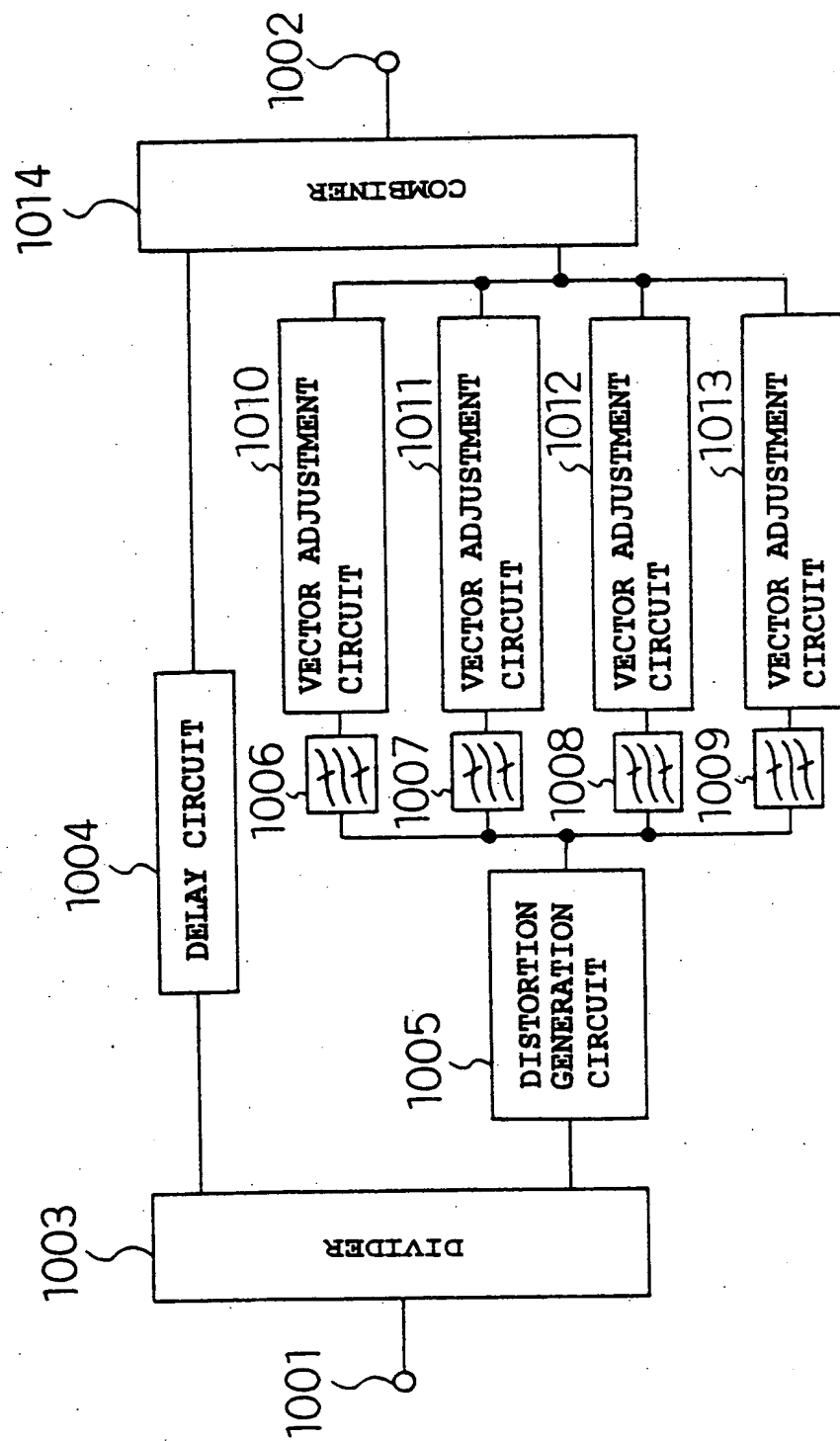


Fig. 11

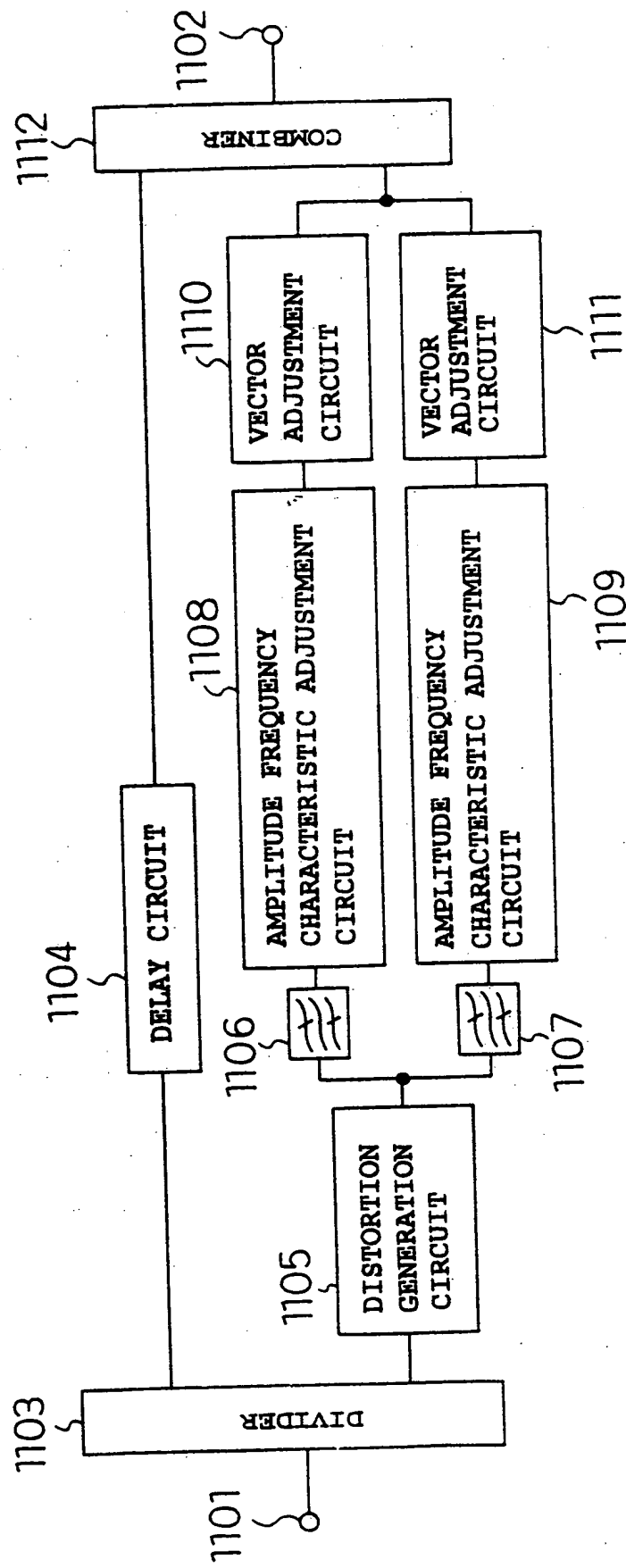


Fig. 12

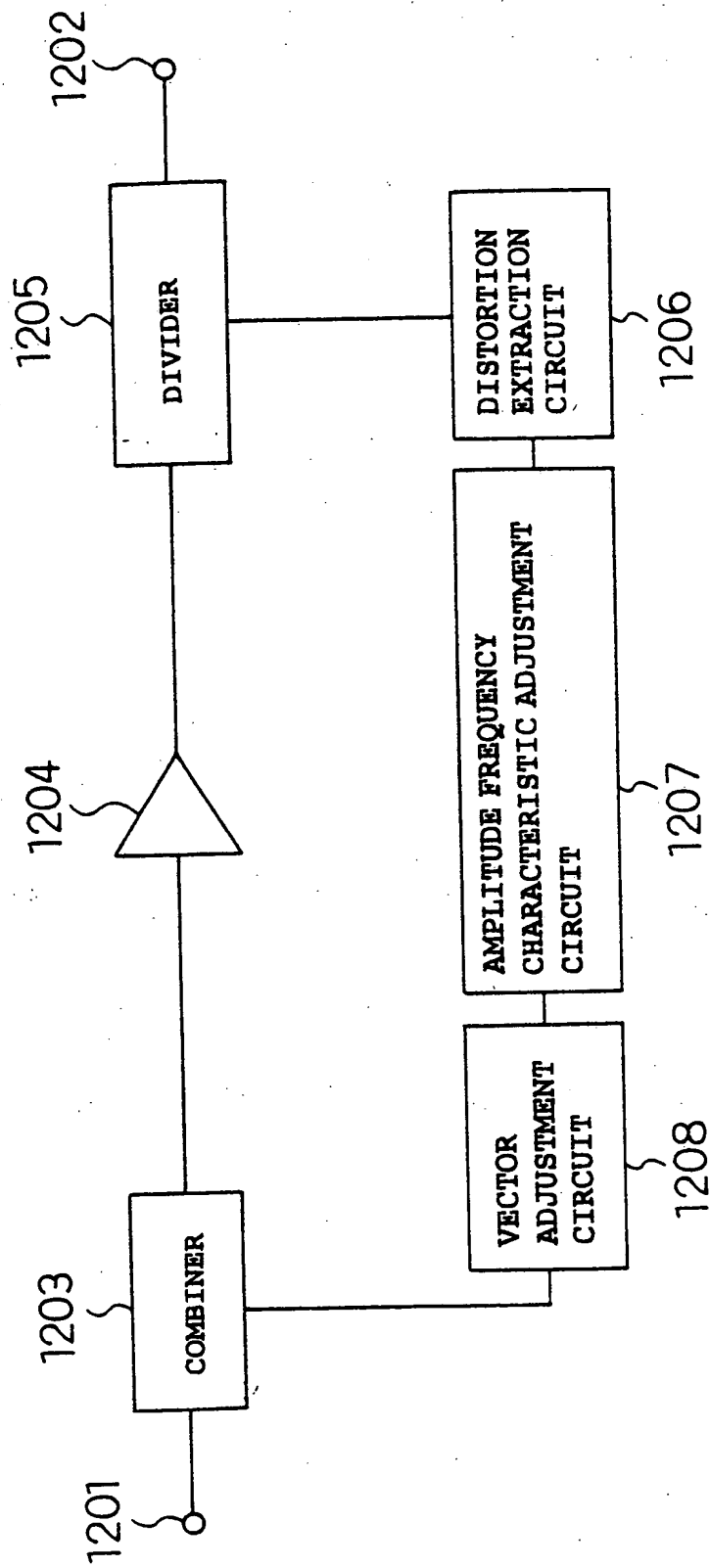


Fig. 13

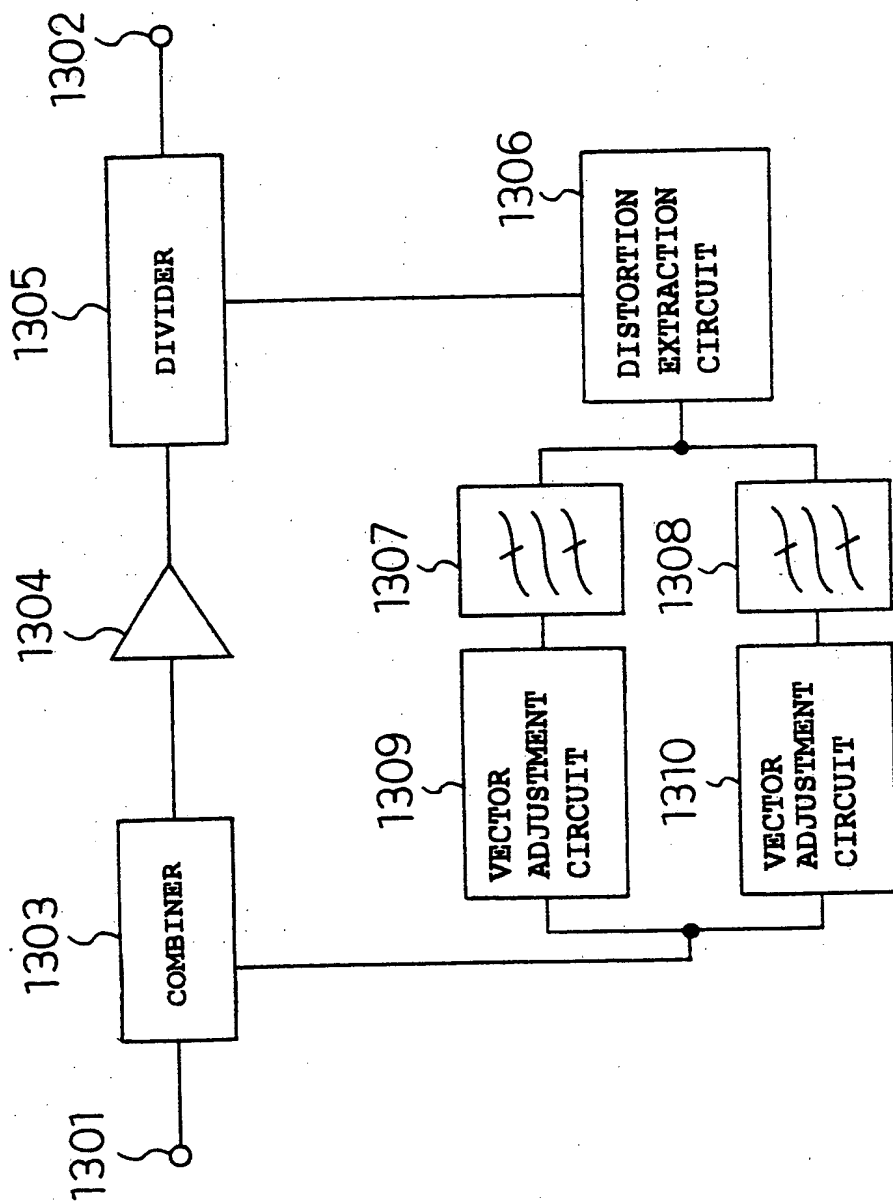


Fig. 14

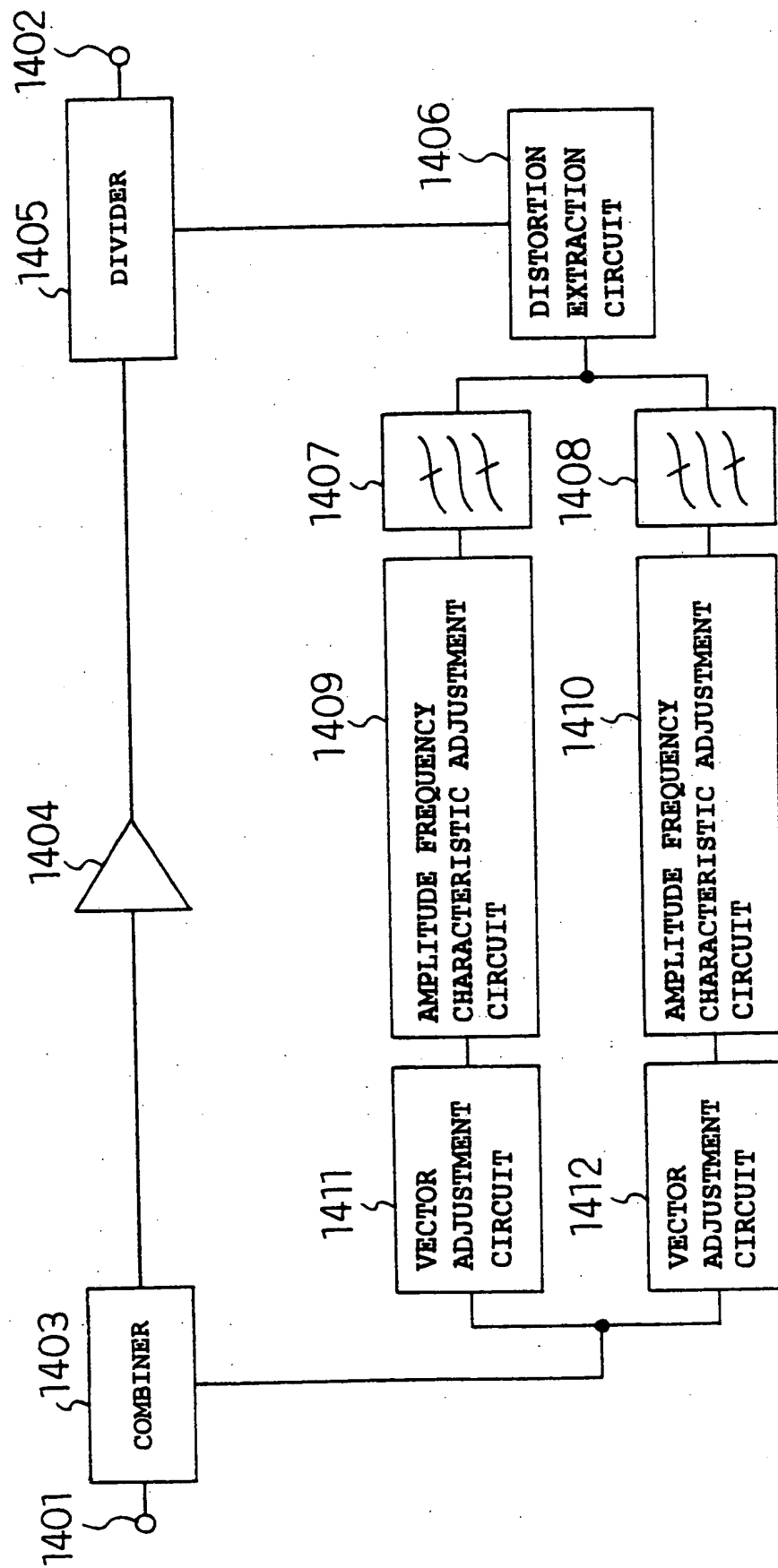


Fig. 15

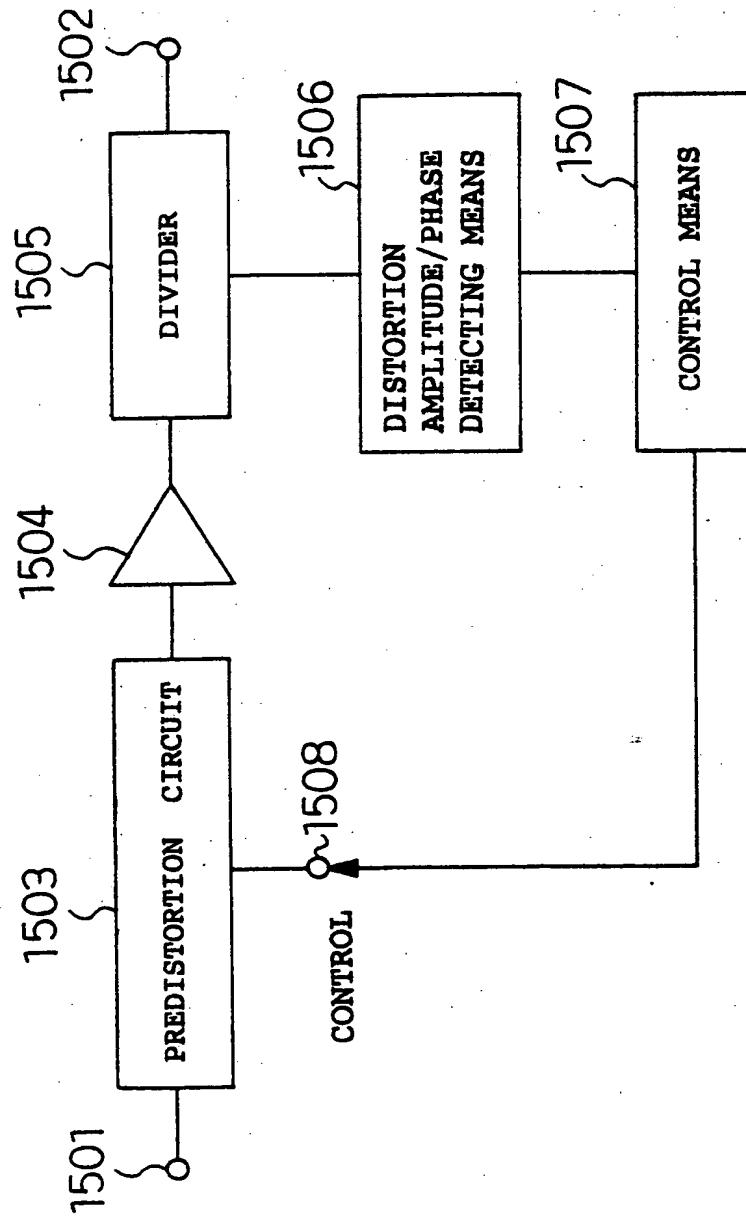


Fig. 16

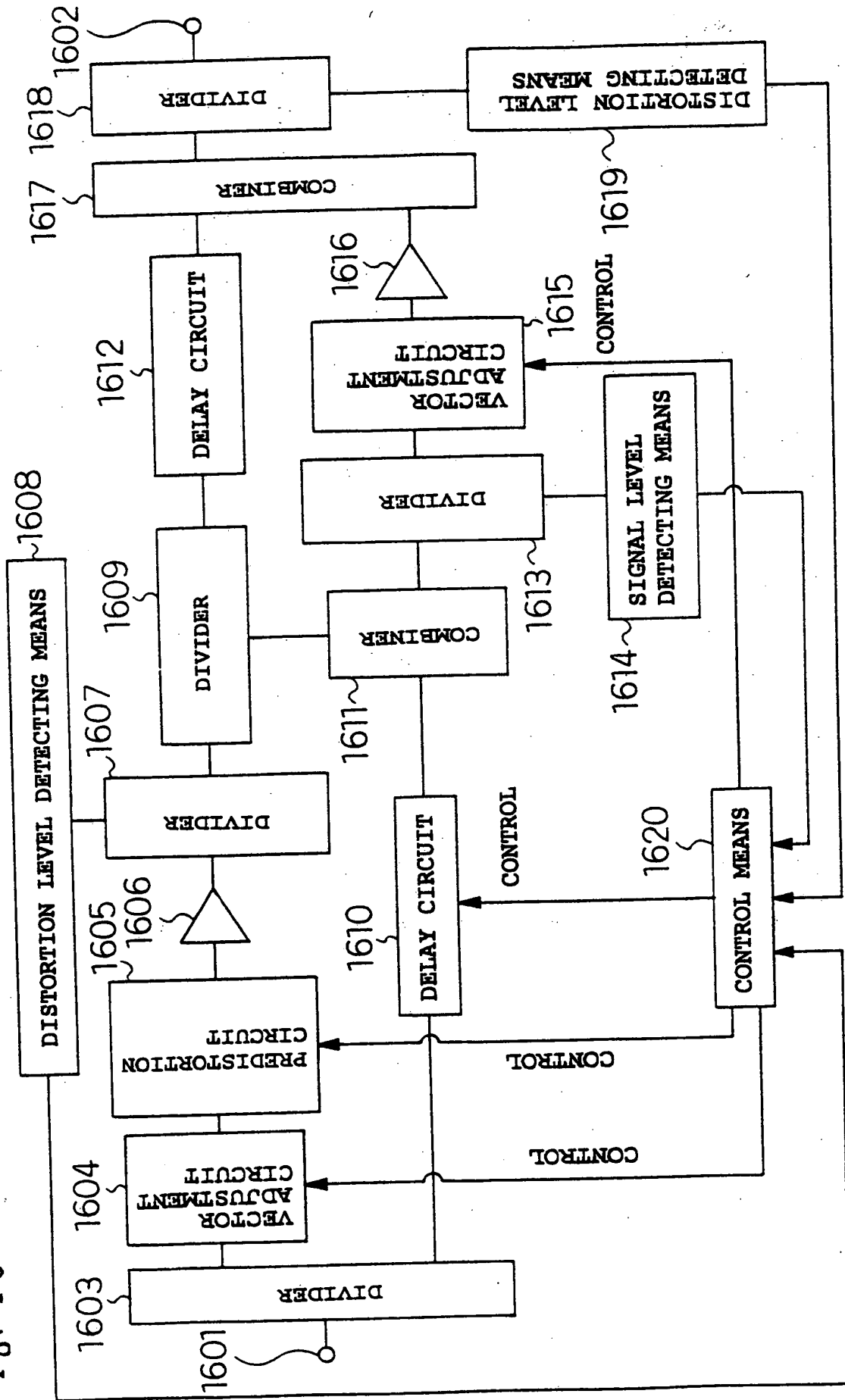


Fig. 17 PRIOR ART

